

EV 318284592

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 10/359,778
Priority Filing Date February 7, 2003
Inventor Warren M. Farnsworth et al.
Assignee Micron Technology, Inc.
Priority Group Art Unit 2182
Priority Examiner Rehana Perveen
Attorney's Docket No. MI22-2420
Title: Computer Including Optical Interconnect, Memory Unit, and Method of Assembling
a Computer

INFORMATION DISCLOSURE STATEMENT

References -- See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation of co-pending Application Serial No. 10/359,778, filed February 7, 2003, upon which the above-identified application relies for a priority date under 35 U.S.C. §120. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2). Applicant submits copies of the articles for review.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: October 17, 2003


Deepak Malhotra
Reg. No. 33,560

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. MI22-2420		SERIAL NO. Filed Herewith		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT WARREN M. FARNWORTH ET AL.				
					FILING DATE Filed Herewith		GROUP 2182		
U.S. PATENT DOCUMENTS									
*Examiner Initial		Document Number	Date	Name		Class	Subclass	Filing Date If Appropriate	
	AA	5,013,247	5/07/91	WATSON					
	AB	5,237,609	8/17/93	KIMURA					
	AC	5,347,384	9/13/94	McREYNOLDS ET AL.					
	AD	5,841,628	11/24/98	ALPERT ET AL.					
	AE	6,249,621 B1	6/19/01	SARGENT, IV., ET AL.					
	AF	6,273,759 B1	8/14/01	PERINO ET AL.					
	AG	6,052,498	4/18/00	PANICCIA					
	AH	5,513,021	4/30/96	KANESHIRO ET AL.					
	AI	5,479,539	12/26/95	GOLDSMITH ET AL.					
	AJ	6,157,966	12/5/00	MONTGOMERY ET AL.					
	AK	6,202,110 B1	3/13/01	COTEUS ET AL.					
	AL								
FOREIGN PATENT DOCUMENTS									
		Document Number	Date	Country		Class	Subclass	Translation	
								Yes	No
	AM								
	AN								
	AO								
	AP								
	AQ								
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)									
	AR		GUILFOYLE, Peter S., et al., "Free-Space Interconnects for High-Performance Optoelectronic Switching". <u>IEEE</u> , pp. 69-75 (Feb. 1998)						
	AS								
	AT								
EXAMINER				DATE CONSIDERED					
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>									

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U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name		Class	Subclass	Filing Date If Appropriate
	AA	5,430,676	7/4/95	WARE ET AL.				
	AB	5,390,308	2/14/95	WARE ET AL.				
	AC	5,355,391	10/11/94	HOROWITZ ET AL.				
	AD	5,319,755	6/7/94	FARMWALD ET AL.				
	AE	5,119,451	6/2/92	WILLS ET AL.				
	AF	5,009,476	4/23/91	REID ET AL.				
	AG	3,978,343	8/31/76	BRODERICK ET AL.				
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	AI	6,328,484 B1	12/11/01	UEBBING				
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	AK	5,475,778	12/12/95	WEBB				
	AL	5,144,691	9/01/92	AUGUST ET AL.				
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AM							
	AN							
	AO							
	AP							
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR		ISHIKAWA, Masatoshi et al., "Optically Interconnected Parallel Computing Systems", <u>IEEE</u> , pp. 61-75 (February 1998)					
	AS		CAUFIELD, "Perspectives in optical Computing", <u>IEEE</u> , pp. 22-25 (1998)					
	AT		MITKAS, Pericles A., et al., "Optical processing Paradigms for Electronic Computers", <u>IEEE</u> , pp. 45-51 (Feb. 1998)					
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U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,680,361	10/21/97	WARE ET AL.			
	AB	5,663,661	9/2/97	DILLON ET AL.			
	AC	5,621,786	4/15/97	FISCHER ET AL.			
	AD	5,602,667	1/2/97	PATEL			
	AE	5,552,921	9/3/96	HETZEL ET AL.			
	AF	5,546,417	8/13/96	GORELIK			
	AG	5,537,573	7/16/96	WARE ET AL.			
	AH	5,499,385	3/12/96	FARMWALD ET AL.			
	AI	5,499,355	3/12/96	KRISHNAMOHAN ET AL.			
	AJ	5,485,490	1/16/96	LEUNG ET AL.			
	AK	5,446,696	8/29/95	WARE ET AL.			
	AL	5,432,823	7/11/95	GASBARRO ET AL.			
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
	AM						Yes
	AN						No
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR		GILLINGHAM, Peter, "SLDRAM Architectural and Functional Overview," <u>SLDRAM Consortium</u> , August 29, 1997, pp. 1-14.				
	AS		LIU, Y.S., "Lighting the Way in Computer Design," Y.S. Liu, <u>Circuits & Devices</u> , January 1998, pp. 23-31.				
	AT		IRANKLIOTIS, Leo et al., "Optics: A Maturing Technology for Better Computing," <u>IEEE</u> , February, 1998, pp. 36-51.				
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